Docket No.: 67160-015 PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of : Customer Number: 20277

Hisaya MORI, et al. : Confirmation Number:

Serial No.: : Group Art Unit:

Filed: August 26, 2003 : Examiner:

For: APPARATUS FOR TESTING SEMICONDUCTOR INTEGRATED CIRCUIT

INFORMATION DISCLOSURE STATEMENT

Mail Stop Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

In accordance with the provisions of 37 C.F.R. 1.56, 1.97 and 1.98, the attention of the Patent and Trademark Office is hereby directed to the documents listed on the attached form PTO-1449. It is respectfully requested that the documents be expressly considered during the prosecution of this application, and that the documents be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

This Information Disclosure Statement is being filed within three months of the U.S. filing date OR before the mailing date of a first Office Action on the merits. No certification or fee is required.

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Respectfully submitted,

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Date: August 26, 2003 WDC99 800470-1.067160.0015

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APPLICATION											
				APPLICANT Hisaya MORI, et al.							
(PTO-1449)					FILING DATE August 26, 2003	1			GROUP		
U.S. PATENT DOCUMENTS											
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		US	US 2002/0062200 A1	05/23/2002	Mori et al.						
		US	US 2003/0048112 A1	03/13/2003	Mori et al.						
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			7.000000						Yes	No	
			-7-209389 (w/ English Abstract)	08/11/1995	ADVANTEST CORP.						
		JP.	2002-6001 (w/ English Abstract)	01/09/2002	ADVANTEST CORP.						
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1 Applicant's unique citation designation number (optional). 2 Applicant is to place a check mark here if English language Translation is attached.